

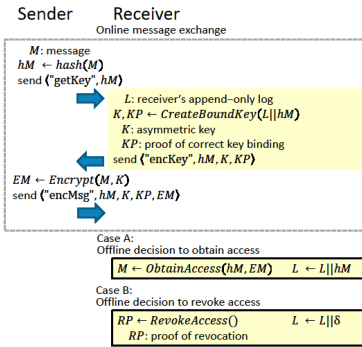
Inserting Intentional Bugs for Model Checking Assurance

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The Problem

- Complicated protocol (“Pasture”)



CreateBoundKey(hM):

```

Rt ← TPM_Read(PCRAApp)
Rt+1 ← SHA1(Rt || hM)
transport session
BINDKEY {
  K ← TPM_CreateWrapKey({
    PCRAApp = Rt+1 &&
    PCRSEM = SemHappy &&
    PCRSEAL = SealReboot })
  α ←
  KP ← ("CreateBoundKey", hM, Rt, Rt+1, α)
    
```

ObtainAccess(hM, EM):

```

append hM to full log
TPM_Extend(PCRAApp, hM)
M ← TPM_Unbind(EM)
    
```

RevokeAccess():

```

Rt ← TPM_Read(PCRAApp)
append δ to full log
TPM_Extend(PCRAApp, δ)
Rt+1, St+1, At+1, α ←
TPM_Quote(PCRAApp, PCRSEM, PCRSEAL)
RP ← ("RevokeAccess", δ, Rt, Rt+1, St+1, At+1, α)
    
```

Audit(nonce):

```

Rt, St, At, α ←
TPM_Quote(PCRAApp, PCRSEM, PCRSEAL, nonce)
AP ← ("Audit", full log, Rt, St, At, nonce, α)
    
```

Recover():

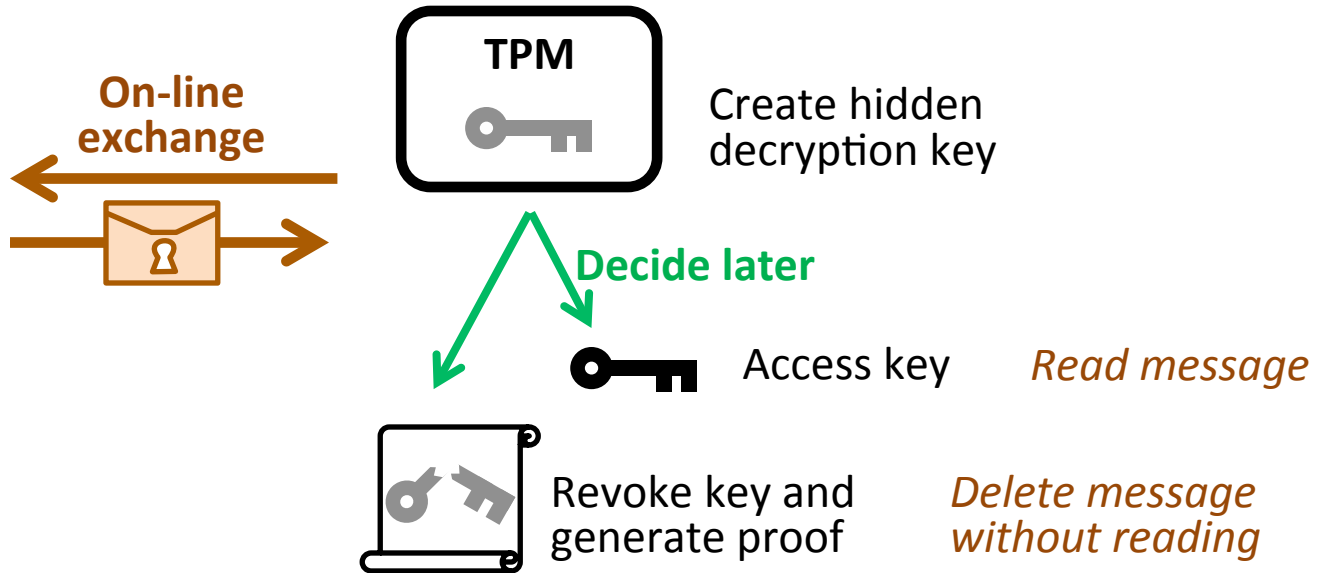
```

FOR EACH entry Δ on full log: TPM_Extend(PCRAApp, Δ)
secure execution mode
IF nv.current && nv.R = TPM_Read(PCRAApp)
THEN
  nv.current ← FALSE
  TPM_Extend(PCRSEM, Happy)
ELSE
  TPM_Extend(PCRSEM, Unhappy)
    
```

Checkpoint():

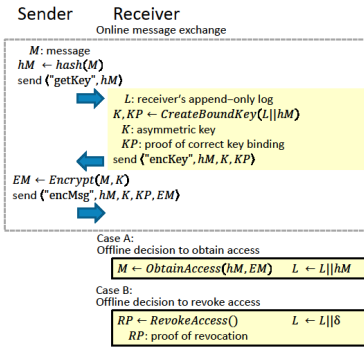
```

transport session
SEAL {
  Rt ← TPM_Read(PCRAApp)
  St ← TPM_Read(PCRSEM)
  At ← TPM_Read(PCRSEAL)
  Ct ← TPM_ReadCounter(CTR)
  α ← TPM_Extend(PCRSEAL, Seal)
  nv.R ← Rt
  IF ValidSEAL(α, Rt, St, At, Ct)
  && St = SemHappy
  && At = SealReboot
  && Ct = TPM_ReadCounter(CTR)
THEN
  TPM_IncrementCounter(CTR)
  nv.current ← TRUE
  TPM_Extend(PCRSEM, Unhappy)
    
```



The Problem

- Complicated protocol (“Pasture”)
- Important safety properties



```

CreateBoundKey(hM):
  R_t ← TPM_Read(PCR_App)
  R_{t+1} ← SHA1(R_t || hM)
  transport session
  BINDKEY {
    K ← TPM_CreateWrapKey({
      PCR_App = R_{t+1} &&
      PCR_SEM = SemHappy &&
      PCR_SEAL = SealReboot })
    α ←
  }
  KP ← ("CreateBoundKey", hM, R_t, R_{t+1}, α)

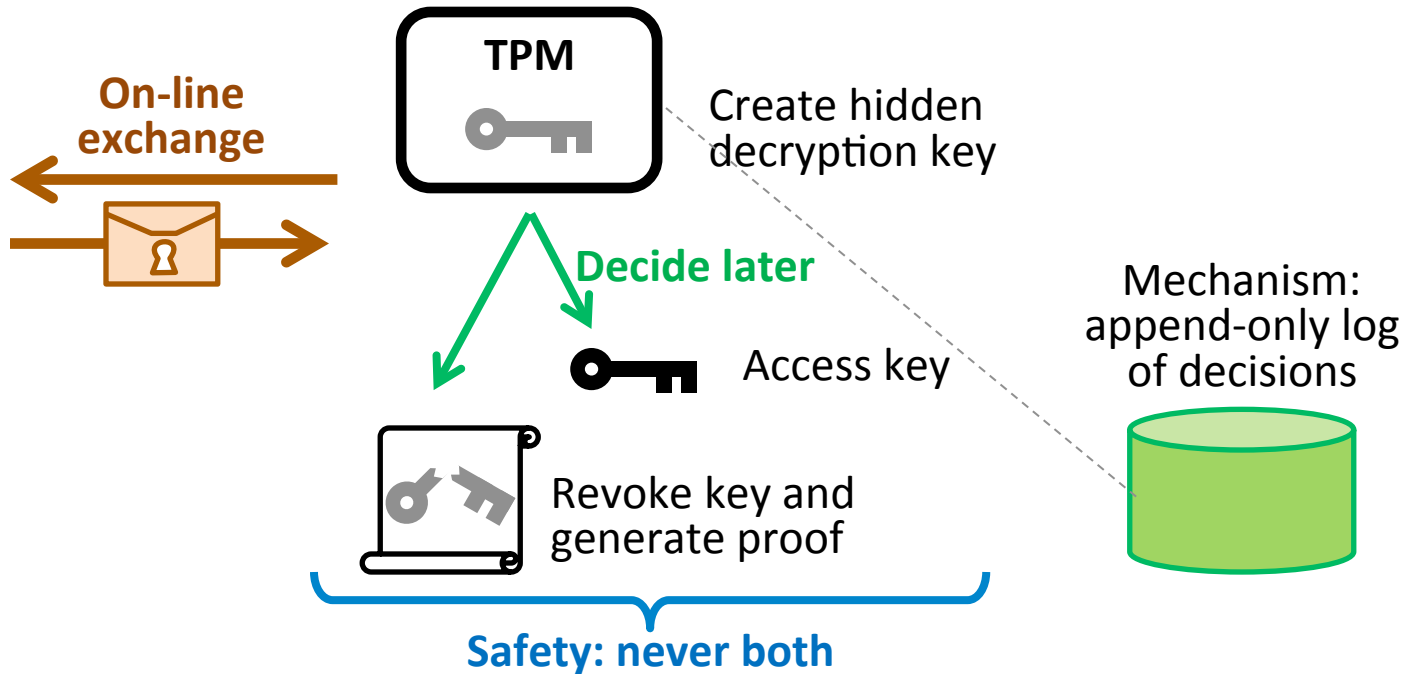
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  append hM to full log
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  M ← TPM_Unbind(EM)

RevokeAccess():
  R_t ← TPM_Read(PCR_App)
  append δ to full log
  TPM_Extend(PCR_App, δ)
  R'_{t+1}, S'_{t+1}, A'_{t+1}, α ←
  TPM_Quote(PCR_App, PCR_SEM, PCR_SEAL)
  RP ← ("RevokeAccess", δ, R_t, R'_{t+1}, S'_{t+1}, A'_{t+1}, α)

Audit(nonce):
  R_t, S_t, A_t, α ←
  TPM_Quote(PCR_App, PCR_SEM, PCR_SEAL, nonce)
  AP ← ("Audit", full log, R_t, S_t, A_t, nonce, α)

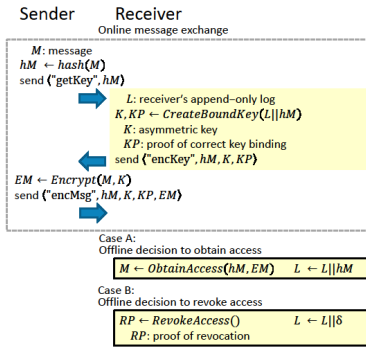
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  FOR EACH entry Δ on full log: TPM_Extend(PCR_App, Δ)
  secure execution mode
  {
    IF nv.current && nv.R = TPM_Read(PCR_App)
    THEN
      nv.current ← FALSE
      TPM_Extend(PCR_SEM, Happy)
    ELSE
      TPM_Extend(PCR_SEM, Unhappy)
  }

Checkpoint():
  transport session
  SEAL {
    R_t ← TPM_Read(PCR_App)
    S_t ← TPM_Read(PCR_SEM)
    A_t ← TPM_Read(PCR_SEAL)
    C_t ← TPM_ReadCounter(CTR)
    α ← TPM_Extend(PCR_SEAL, Seal)
  }
  secure execution mode
  {
    nv.R ← R_t
    IF Valid_SEAL(α, R_t, S_t, A_t, C_t)
    && S_t = SemHappy
    && A_t = SealReboot
    && C_t = TPM_ReadCounter(CTR)
    THEN
      TPM_IncrementCounter(CTR)
      nv.current ← TRUE
      TPM_Extend(PCR_SEM, Unhappy)
  }
    
```



The Problem

- Complicated protocol (“Pasture”)
- Important safety properties
- Adversarial setting



```

CreateBoundKey(hM):
  R_t ← TPM_Read(PCR_App)
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    PCR_App = R_{t+1} &&
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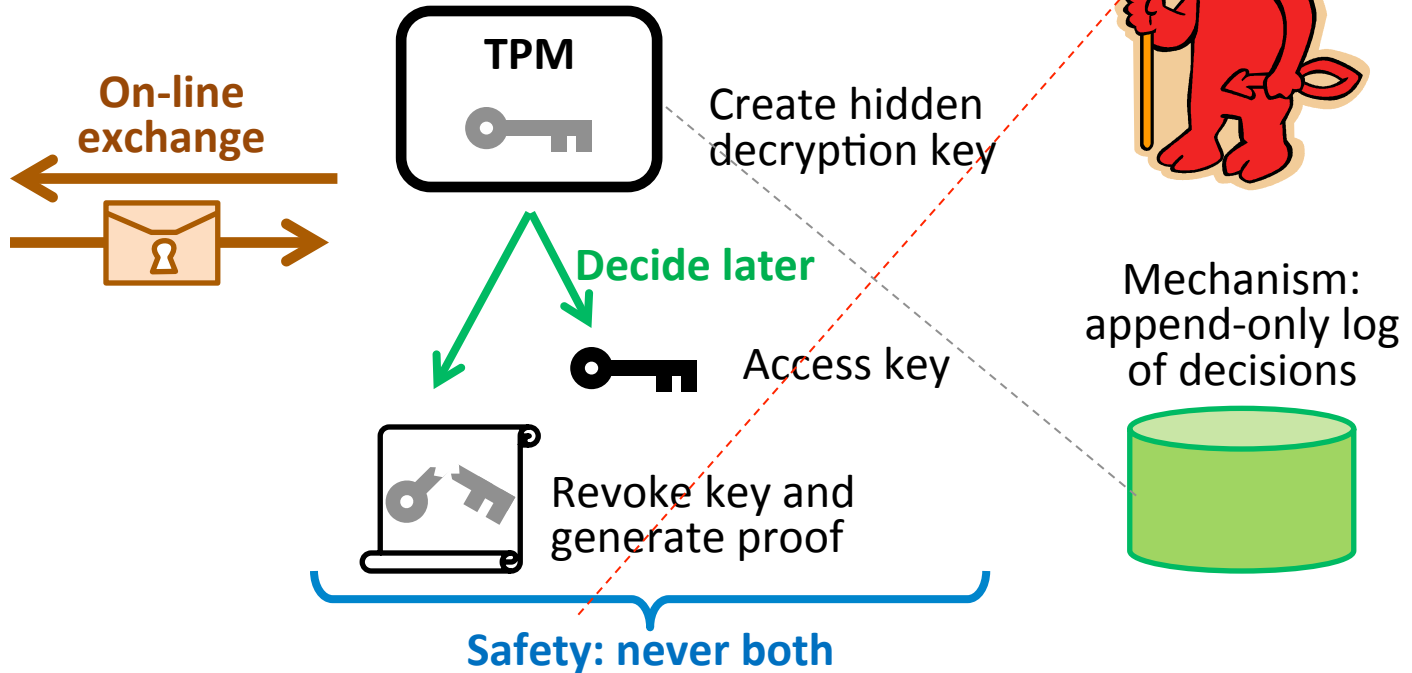
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  append δ to full log
  TPM_Extend(PCR_App, δ)
  R'_{t+1}, S'_{t+1}, A'_{t+1}, α ←
    TPM_Quote(PCR_App, PCR_SEM, PCR_SEAL)
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Audit(nonce):
  R_t, S_t, A_t, α ←
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  AP ← ("Audit", full log, R_t, S_t, A_t, nonce, α)

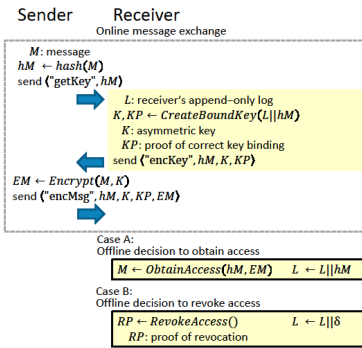
Recover():
  FOR EACH entry Δ on full log: TPM_Extend(PCR_App, Δ)
  IF nv.current && nv.R = TPM_Read(PCR_App)
  THEN
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    TPM_Extend(PCR_SEM, Happy)
  ELSE
    TPM_Extend(PCR_SEM, Unhappy)

Checkpoint():
  R_t ← TPM_Read(PCR_App)
  S_t ← TPM_Read(PCR_SEM)
  A_t ← TPM_Read(PCR_SEAL)
  C_t ← TPM_ReadCounter(CTR)
  α ← TPM_Extend(PCR_SEAL, Seal)
  nv.R ← R_t
  IF Valid_SEAL(α, R_t, S_t, A_t, C_t)
  && S_t = SemHappy
  && A_t = SealReboot
  && C_t = TPM_ReadCounter(CTR)
  THEN
    TPM_IncrementCounter(CTR)
    nv.current ← TRUE
    TPM_Extend(PCR_SEM, Unhappy)
    
```



The Solution

- Complicated protocol (“Pasture”)
- Important safety properties
- Adversarial setting
- Solution: Use formal methods
 - Specification – *is it correct?*
 - Model checking – *was it enough?*
 - Formal proof – *too hard?*



CreateBoundKey(hM):

transport session

```

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    PCRApp = Rt+1 &&
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RevokeAccess():

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Rt ← TPM_Read(PCRApp)
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TPM_Extend(PCRApp, δ)
Rt+1, St+1, At+1, α ←
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Audit(nonce):

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Recover():

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    TPM_Extend(PCRSEM, Unhappy)
    
```

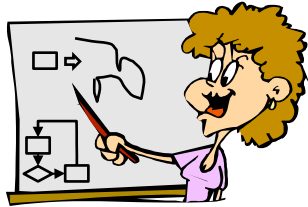
Checkpoint():

transport session

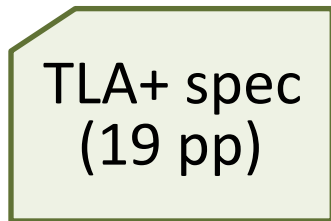
```

    SEAL {
    Rt ← TPM_Read(PCRApp)
    St ← TPM_Read(PCRSEM)
    At ← TPM_Read(PCRSEAL)
    Ct ← TPM_ReadCounter(CTR)
    α ← TPM_Extend(PCRSEAL, Seal)
    }
    secure execution mode {
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    IF ValidSEAL(α, Rt, St, At, Ct)
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    && Ct = TPM_ReadCounter(CTR)
    THEN
    TPM_IncrementCounter(CTR)
    nv.current ← TRUE
    TPM_Extend(PCRSEM, Unhappy)
    
```

What we did for Pasture



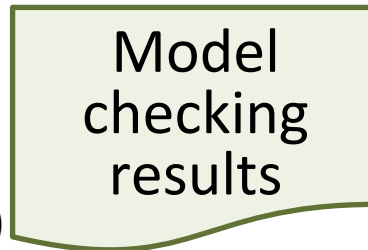
Proof
sketch



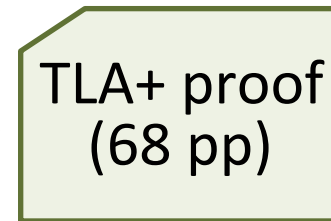
state, actions, invariants



(CPU months)



Write proof
(2 weeks)



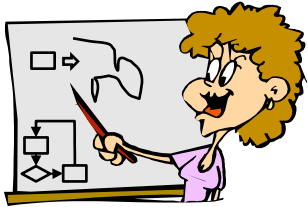
invariants



Understanding
the results

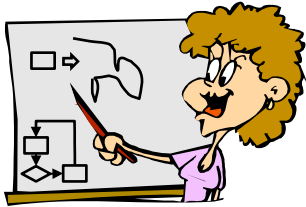
Identical logic but
tons more detail

Pretty sure it is correct



Proof
sketch

Pretty sure we covered everything

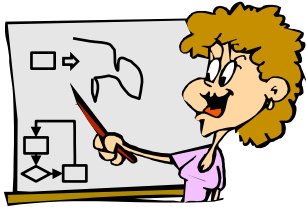


Proof
sketch

TLA+ spec
(19 pp)

state, actions, invariants

Model checking – was it enough?



Proof
sketch

TLA+ spec
(19 pp)

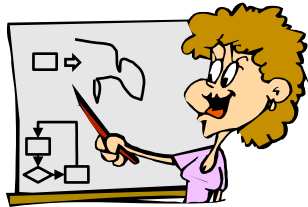
state, actions, invariants



Several CPU months later...
“no errors found”

Cannot model check any larger configurations using TLC because such configurations have more than 2^{32} distinct states – making state fingerprint collision a near certainty.

Insert some intentional bugs



Proof sketch

Easy methodology: Find an action that seems important and omit it

TLA+ spec
(19 pp)

state, actions, invariants



Several CPU months later...
“no errors found”

+ *intentional bugs*

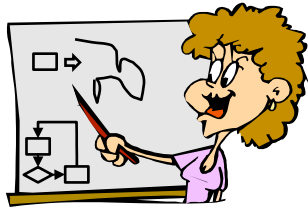
TLA+ spec
(19 pp)



A few CPU minutes...

Bug1	violation example
Bug2	violation example
Bug3	violation example
...	...
Bug12	violation example
Bug13	violation example
Bug14	no error
Bug15	no error
Bug16	no error

Not all bugs violate safety



Proof sketch

TLA+ spec (19 pp)

state, actions, invariants



Several CPU months later...
"no errors found"



+ intentional bugs

TLA+ spec (19 pp)



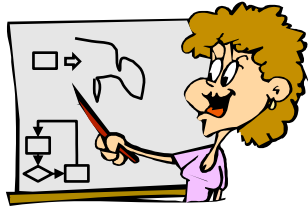
A few CPU minutes...

Bug1	violation example
Bug2	violation example
Bug3	violation example
...	...
Bug12	violation example
Bug13	violation example
Bug14	no error
Bug15	no error
Bug16	no error

After analysis:
these bugs
happen not to
violate safety

----- violates liveness
----- violates append-only log
----- violates append-only log

Now write the formal proof



Proof
sketch

TLA+ spec
(19 pp)

state, actions, invariants



(CPU months)

Model
checking
results



Write proof
(2 weeks)

TLA+ proof
(68 pp)

invariants

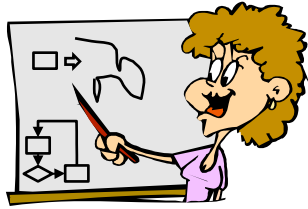


Understanding
the results

Identical logic but
tons more detail

*Proof does not permit the
append-only log violation bugs.*

Include a slight optimization



Proof sketch

TLA+ spec (19 pp)

state, actions, invariants



(CPU months)

Model checking results



Write proof (2 weeks)

TLA+ proof (68 pp)

invariants



Slightly optimize implementation

Modified TLA+ spec (17 pp)

state, actions, invariants



Revise proof (2 hours)

Modified TLA+ proof (64 pp)

invariants

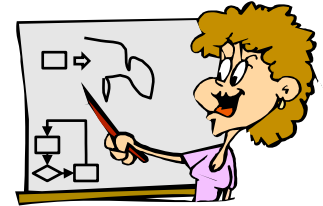


Understanding the results

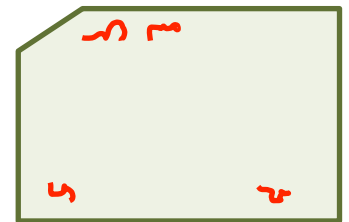
Identical logic but tons more detail

Conclusions

- Proof sketch was valuable
 - Helped understand model results
 - Guided formal proof



- Assurance via intentional bugs before proof
- Better to specify the actual invariant, not the (stronger) proof invariant
- Amazingly easy to create proof for slightly modified specification



invariants