Exposing Design Flaws in Shared-Clock Systems using TLA+

Russell Mull, Auxon Corporation TLA+ Conf September 12, 2019

About Me

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- Decide how much it matters (Assign a Safety Integrity Level SIL)
- Analyze the parts of the system that matter (Fault Tree Analysis)
- Not good enough? Add redundancy.



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- SIL: 4
- Fault tree: the actuator is only SIL 3
- Redundancies: use two, design a SIL 4 failover mechanism

Functional Safety

• IEC 61508

• Power plants, chemical plants, cars, trains, heavy machinery, etc.

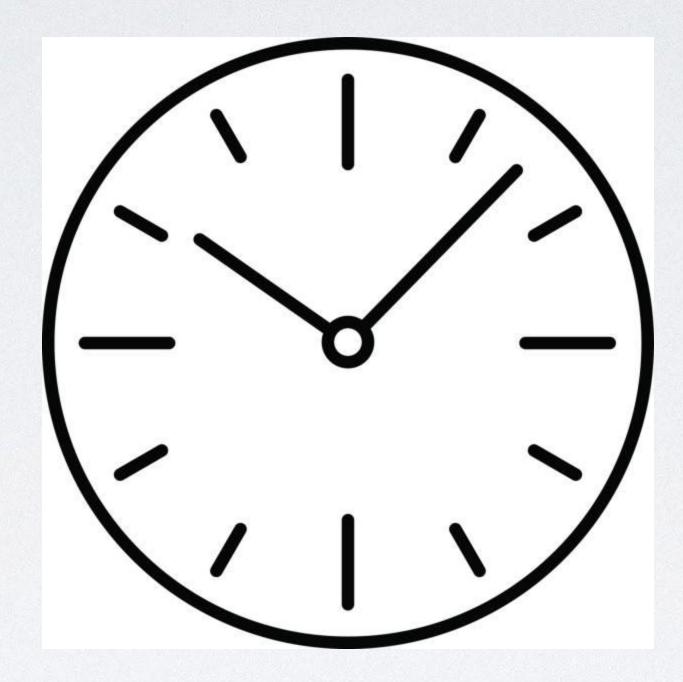






This works well, until...

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In software, shared clock failures are lumpy and unpredictable

The story of a system made from lots of computers, sensors, actuators, and clocks

• Can't say anything specific

- Can't say anything specific
- Relies fundamentally on a common timebase



- Can't say anything specific
- Relies fundamentally on a common timebase
- Appeared to be vulnerable to drift



My Goal: Demonstrate the problem

VARIABLES node_clock, system
Nodes == { "A", "B", "C" }

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 $/\ node_clock = [n \ Nodes |-> 0]$ $/ \ system = \ldots$

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/\ UNCHANGED << node clock >>

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Next == $\ \ E$ node $\ DOMAIN$ node clock: /\ node clock' = [node clock EXCEPT ![node] = @ + 1] /\ UNCHANGED << system >> // / SystemStep(system) /\ UNCHANGED << node clock >> \/ SyncClocks(node clock, system)

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SystemStep(s) == ... $SyncClocks(cs,s) == \dots$

This approach is not great.

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Massive state explosion

This approach is not great.

- Massive state explosion

Customer doesn't care about the sync protocol

Model the drift, not the sync

Drift Modeling (1)

CONSTANTS SIMULATED_CYCLES, BOUNDED_DRIFT VARIABLES node clock, system, global_clock

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\/ SystemStep /\ UNCHANGED global clock /\ UNCHANGED node clock

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\/ SystemStep /\ UNCHANGED global clock /\ UNCHANGED node clock

SystemStep == ...

ClockStep ==

ClockStep ==

* Tick the global clock

 $// // global clock' = global_clock + 1$

/\ UNCHANGED << node clock >>

/\ ClockDriftInBounds(global_clock', node_clock)

ClockStep ==

- * Tick the global clock
- // / global clock' = global clock + 1
 - /\ UNCHANGED << node clock >>
 - /\ ClockDriftInBounds(global clock', node clock)

 $\ \ tau$ Tick a node clock $\ \ E$ node $\ DOMAIN$ node clock: /\ node clock' = [node clock EXCEPT ![node] = @ + 1] /\ UNCHANGED << global clock >> /\ ClockDriftInBounds(global clock, node clock')

ClockDriftInBounds(g, n) == /\ g <= SIMULATED CYCLES $/ \ A node \ in DOMAIN n :$ /\ n[node] <= SIMULATED_CYCLES</pre> /\ Abs(c[node] - g) <= BOUNDED DRIFT</pre>

This works better

This works better

• Narrower state space

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• Narrower state space

• Directly addresses relevant failure domain

The system was more vulnerable to drift than previously thought

Delivering a Model

- Literate PDF
- Makefile / .cfg file
- Config Instructions

TLA+ is tricky to use this way

- Difficult setup
- Easier development
- Easier delivery

Give models to your customers

• Asymmetric Drift

Action on Tick

Cyclical Clock

Extending the technique

• Fake a real clock

• Fake a real clock

• Bound the drift

- Fake a real clock
- Bound the drift
- Give models to your customers

- Fake a real clock
- Bound the drift
- Give models to your customers
- I owe Hillel Wayne a great debt

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